

Use Cortex-M0?

Start from megawin!



Auto Reclosing Device(ARD)



Meter



Battery Management System



Elevator control

MG32F02A Base Line Series(Cortex-M0)

MG32F02U USB Line Series(Cortex-M0)

Item	Vdd	Flash ROM	Data RAM	Max. Freq.	Timer (16-BIT)	IO	12Bit ADC	Comp	Inter-Face	CCP ^{*4}	ISP/IAP	Package
MG32F02A032 ^{*1}	1.8V~5.5V	32KB	4096B	48MHz	5+RTC	17/29/44	12-CH	2	UART ^{*2} x2, (I ² C) SPI/QPI,PWM, CRC,DMA	4-CH	YES ^{*3}	TSSOP20 QFN32 LQFP48
MG32F02A064 ^{*1}	1.8V~5.5V	64KB	8192B	48MHz	7+RTC	44/59	16-CH	2	UART ^{*5} x7, (I ² C)x2 SPI/QPI/OPI,CRC, DMA,DAC	8-CH	YES ^{*3}	LQFP48 LQFP64
MG32F02U064 ^{*1}	1.8V~5.5V	64KB	16384B	48MHz	7+RTC	44/59	16-CH	2	UART ^{*5} x7, (I ² C)x2 USB/SPI/QPI/OPI,CRC, DMA,DAC	8-CH	YES ^{*3}	LQFP48 LQFP64
MG32F02A072 ^{*1}	1.8V~5.5V	72KB	8192B	48MHz	7+RTC	44/59	16-CH	4	UART ^{*2} x4, (I ² C)x2 SPI/QPI/OPI,CRC, DMA	8-CH	YES ^{*3}	LQFP48 LQFP64
MG32F02A128 ^{*1}	1.8V~5.5V	128KB	16384B	48MHz	7+RTC	59/73	16-CH	2	UART ^{*5} x7, (I ² C)x2 SPI/QPI/OPI,CRC, DMA,DAC	8-CH	YES ^{*3}	LQFP64 LQFP80
MG32F02U128 ^{*1}	1.8V~5.5V	128KB	16384B	48MHz	7+RTC	59/73	16-CH	2	UART ^{*5} x7, (I ² C)x2 USB/SPI/QPI/OPI,CRC, DMA,DAC	8-CH	YES ^{*3}	LQFP64 LQFP80
MG32F02A132 ^{*1}	1.8V~5.5V	132KB	16384B	48MHz	7+RTC	59/73	16-CH	4	UART ^{*2} x4, (I ² C)x2 SPI/QPI/OPI,CRC, DMA	8-CH	YES ^{*3}	LQFP64 LQFP80

^{*1} Support M-LINK ICE;

^{*2} All UART support SPI Master;

^{*3} Share with all Flash zone;

^{*4} CCP: Input Capture/Output Compare/PWM.

^{*5} Advanced UART Support SPI Master/Slave.